CURRICULUM VITAE

Vasava Niteshbhai Prabhatsinh, 279/ A, Shaligram Greens, Rambhai kaka marg, Bakrol, V V Nagar 388315 Gujarat



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Objective		
To find challenging position in the organization where I can serve using my competencies, skills and education Strength		
 Good organizational and interpersonal skills 		
• Able to deal with people diplomatically		
 Willingness to learn, team facilitator and hard worker 		
 Working innovates with different resources 		
 Good Co-ordinator skills 		
 Good Analytical skills 		
Academic Qualifications		
B. E. (Electronics & Comm.) Govt. Engg. College Modasa (H N G U, Patan) Passed in June		
2007.		
M.E. (Communication System Engg.) L.D. College of Engineering Ahmedabad (G.T.U.) passed		

Dissertation

• Simulation of Turbo Codes for Uplink WCDMA (3G)

Publications:

in June 2011.

- Capacity improvement with Turbo Codes in the WCDMA enhanced uplink channel
- A Comparative study of Next Generation Wireless Communication

Paper published at National Conference on Recent Trends in Engineering and Technology (NCRTET 2011) at BVM Engineering College, Vidhyanagar, Gujarat *Published on 13-14 May 2011, ISBN No. 987-81-921358-3-0*

Academic Experience & Co-Curricular Activities

➢ Total Teaching experience 12 Years.

Area of Interest:

- Embedded System
- IoT- Internet of things
- Advance Controllers / Processors
- Basic Electronics
- Electronics Communication
- Data Communication and Networking
- Life Member of ISTE (LM-93089)
- Attend Two week ISTE workshop on Introduction to Research Methodologies Jointly Organized by IIT Bombay
- Attend Two day ISTE workshop on Aakash for Education Jointly Organized by IIT Bombay
- Attend Two week ISTE workshop on Signals & Systems Jointly Organized by IIT Bombay
- Attend One day workshop on Project Management organized at MBICT
- Attend One day workshop on Moodle at ISTAR
- Attend One day Seminar on Design Engineering Jointly with GIC at MBICT
- Attend Two week ISTE workshop on Control Systems Jointly Organized by IIT Kharagpur
- Attend one week ISTE STTP on Advanvance microcontroller and its Application Organized by BVM Engg. College V V Nagar.
- Arrange three day workshop on Arduino and its Application organize by B& B Inst.of Tech.
 V V Nagar.

Personal Information

Full Name Date of Birth Gender Marital Status Birth-Place Nationality	 Vasava Niteshbhai Prabhatsinh 1 June 1983 Male Married Balalkuva Indian
Gender Marital Status Birth-Place	: Male : Married : Balalkuva : Indian

References: 1) Prof. M.Y.Kanthariya I/C H.O.D EC Dept. B & B Institute of Technology V V Nagar 2) Prof. Deven TrivediAsst. Prof. EC Dept.GCET Engg. College BakrolV V Nagar.