

Kunal J. Pithadiya
Lecturer, EC Department (GIA)
B & B Institute of Technology,
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Education Qualification

Government Polytechnic, Gandhinagar

Diploma Engineering: Electronics & Communication Engg, First class with Dist.,1999

Final Year Projects:

- “Microprocessor 8085 based school bell timer with Real Time Clock (RTC 5832)”
- “Window detector” at Physical Research Laboratory, Ahmedabad

L.D. College of Engineering, Ahmedabad, Gujarat University

Bachelor of Engineering: Electronics & Communication Engg., First class with Dist.,2002

Final Year Project:

- “Interface and Comm. between Temperature monitor (LAKE SHORE) and PLC”
at Institute for Plasma Research, Bhat, Gandhinagar

G H Patel College of Engineering & Technology, V V Nagar, Sardar Patel University

Master of Engineering: Communication Engineering, First class with Distinction, 2009

Thesis: Optimal Edge Detection Methods For Machine Vision Applications

Guided By: Dr. Chintan K. Modi, Ex. HOD, EC Dept., GCET, V V Nagar

Teaching Experience

B & B Inst. Of Technology, V V Nagar

20 Yrs

Lecturer,

02-04-2003 to Present

Teaching Subjects:

Principle of Electronic Comm.,
Microprocessor & Microcontroller, Circuit Design Tools,SP
Embedded System & Microcontroller Architecture, IoT, VLSI

Subjects of Interest:

Image Processing, VLSI, ASIC, Signal Processing,
Embedded System, Machine Vision

Soft Skills:

Assembly Language, C Language, VHDL,
MATLAB, OpenCV

Publication Details

International Journal Papers:

1. Jayesh D Chauhan, Chintan K Modi and **Kunal J. Pithadiya**, “Selecting most favorable rho function for robust M estimator for surface roughness estimation using machine vision”, International Journal of Advanced Manufacturing Systems (IJAMS) Vol-1, No 1, 2010, pp-67-74, International Science Press.
2. **Kunal J. Pithadiya**, Chintan K Modi and Jayesh D Chauhan , “Selecting the Most Favourable Edge Detection Technique for Liquid Level Inspection in Bottles”, International Journal of Computer Information Systems and Industrial Management Applications. (IJCISIM), Volume 3, 2011, pp: 34-44, ISSN: 2150-7988, <http://www.miralabs.org/ijcisim>.
3. Dr. Chintan K. Modi, Jayesh D. Chauhan, and **Kunal J. Pithadiya**, “Selecting Most Favorable Robust Estimator for Surface Roughness Estimation Using Machine Vision”, Journal of Manufacturing Technology Research (JMTR), Nova Publisher, Volume 3, Issue: 3-4, 2012 , ISSN: 1943-8095, <https://www.novapublishers.com>.

International Conference Papers:

1. **Kunal J. Pithadiya**, Chintan K. Modi, Jayesh D. Chauhan, “Comparison of Optimal Edge Detection Algorithms for Liquid Level Inspection in Bottles”, Proceedings of the International Conference on Emerging Trends in Engineering & Technology (ICTET-09), pp. 447-452, IEEE Computer Society Washington, DC, USA ©2009, ACM Digital Library , ISBN: 978-0-7695-3884-6.
2. Jayesh D. Chauhan, Chintan K. Modi, **Kunal J. Pithadiya**, “Comparison of Redescending and Monotone M Estimator for Surface Roughness Estimation Using Machine Vision”, Proceedings of the International Conference on Emerging Trends in Engineering & Technology 2009 (ICTET-09), pp. 464-469, IEEE Computer Society Washington, DC, USA ©2009, ACM Digital Library , ISBN: 978-0-7695-3884-6.
3. **Kunal J. Pithadiya**, Chintan K. Modi, Jayesh D. Chauhan, “Quality Inspection of Surface Mount Capacitor Using Optimal Edge detector”, Proceedings of the International Conference on Signals, Systems and Communication 2009 (ICSSC-2009), Anna University, Chennai.
4. Jayesh D. Chauhan, Chintan K. Modi, **Kunal J. Pithadiya**, “Location M estimator for liquid level inspection using Machine Vision”, Proceedings of the International Conference on Signals, Systems and Communication 2009 (ICSSC-2009), Anna University, Chennai.
5. K. R. Jain, Chintan K. Modi, **Kunal J. Pithadiya**, “ Non Destructive quality evaluation in spice reference to Cumin Cyminum L (Cumin) seeds”, International Conference on Innovative Technologies in Intelligent Systems and Industrial Applications (CITISIA 2009) IEEE, pp-311-316, Kuala Lumpur, Malaysia.

6. Kavindra Jain, Chintan K. Modi, **Kunal J. Pithadiya** , “Non Destructive Quality analysis in spice industry with specific reference to *Foeniculum vulgare* (Fennel) seeds.”, International Conference on Visualization, Imaging & Image Processing (VIIP-2009), organized by International Association of Science & Technology for Development (IASTED) ,CAMBRIDGE UNIVERSITY,LONDON,UK.
7. Jayesh D. Chauhan, Chintan K Modi, Kavindra Jain and **Kunal J. Pithadiya**, “Robust M Estimator for Surface Roughness Estimation Using Machine Vision”, Proceedings of the International Conference on Advances in Mechanical Engineering, pp-810 (ICAME-2009) 2009, S.V. National Institute of Technology, Surat, Gujarat, India.
8. **Kunal J. Pithadiya**, Chintan K. Modi, Jayesh D. Chauhan, “Machine vision based liquid level inspection system using ISEF edge detection technique”, Proceedings of the International Conference and Workshop on Emerging Trends in Technology 2010 (ICWET '10),pp- 601-605,ACM Digital Library 2010, ISBN 978-1-60558-812-4.
9. Jayesh D. Chauhan, Chintan K. Modi, **Kunal J. Pithadiya**, “Location M estimator with optimal edge detector for quality inspection of surface mount device capacitor”, Proceedings of the International Conference and Workshop on Emerging Trends in Technology 2010 (ICWET '10),pp- 595-600,ACM Digital Library 2010, ISBN 978-1-60558-812-4.

National Conference Paper:

1. **Kunal J. Pithadiya**, Chintan K. Modi, J. D. Chauhan, Kavindra Jain, “Performance Evaluation of ISEF and Canny Edge detector in Acrylic fiber quality control production”, National Conference on “Innovations in Mechatronics Engineering 2009” (IME-09) ,GCET, V V Nagar, Gujarat, India.

Academic and Departmental Activities

- Life Member of ISTE (LM-39220)
- Life Member of IE
- Worked as a First Scrutiny officer, ACPDC, Ahmedabad,
- Worked as a Syllabus Revision committee member, CDC (GTU) , Ahmedabad
- Worked as a Committee member NBA Accreditation, EC Department,
- Worked as a Project Guide for the Project Exhibition (CREATO) , BBIT, V V Nagar
- Worked as a Project Guide for the Project Exhibition , ADIT, V V Nagar
- Worked as a Co-coordinator of STTP based on “Multisim” ,EC Department, BBIT
- Worked as a III cell member , EC Department, BBIT
- Worked as a Student Central committee member, BBIT
- Worked as a NSS Coordinator, EC Department, BBIT

References

Dr. Chintan K. Modi

Instrumentation and Control Engineering
Technologist, An IIT Bombay Alumni, Langtree
Controls, Ontario, Canada
Former HOD, EC Dept., GCET, V V Nagar
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Former Principal,
B. & B. Institute of Technology,
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