

CURRICULUM VITAE

Muktesh P. Shah, M.E. (GOLD MEDALIST)

Lecturer

Bhailalbai & Bhikhabhai Institute of Technology

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EDUCATION

- 2012/2014 G.H.PATEL COLLEGE OF ENGINEERING & TECHNOLOGY
GUJARAT TECHNOLOGICAL UNIVERSITY, Gujarat, India
M.E. (Communication Engineering) CPI: 9.36,
Gold Medalist for securing Highest CPI at GTU
- 2001/2005 SHRI S'AD VIDYA MANDAL INSTITUTE OF TECHNOLOGY
VEER NARMAD SOUTH GUJARAT UNIVERSITY, Gujarat, India
B.E. (Electronics & Communication Engineering) 71.61 %

PROFESSIONAL APPOINTMENTS

- 2007 – Present BHAILALBHAI & BHIKHABHAI INSTITUTE OF TECHNOLOGY
Vallabh Vidyanagar, Gujarat, India
**Lecturer, (Wireless Communication, Antennas, Metamaterials,
Microprocessor)**
- 2005 – 2006 CHROMATOGRAPHY & INSTRUMENTS COMPANY
121-122, GIDC, Makarpura, Vadodara, Gujarat, India
**Production cum Service Engineer (Testing, Servicing
& Maintenance of GC)**

PUBLICATIONS

Journals

1. Shah, M. M. P., Patel, S. K., Ardesana, M. A., & Patel, M. J. M., "Design of Multiband Microstrip Radiating Structure for C band Application", International Journal of Advanced Research in Computer and Communication Engineering, vol. 2, no. 12, pp. 4560-4563, December 2013 (ISSN : 2319-5940)

Patent

Title of the Invention	CURVE TOOTH COMPLEMENTARY SPLIT RING RESONATOR
Application No.	4156/MUM/2014 A
Date of filing of Application	25/12/2014
Publication Date	16/01/2015
Name of Inventors	Muktesh P. Shah (Main Author) , Shobhit K. Patel, Jigar M. Patel

Books:

1. **“Design of Multiband Microstrip Patch Antenna”**, Jigar Patel, Shobhit Patel & Muktesh Shah
Publication: LAP LAMBERT Academic Publishing, 2016
ISBN: 978-3-330-01686-6

WORKSHOP / TRAINING PROGRAMS / SEMINAR / STTP ATTENDED / ARRANGED

1. Attended AICTE sponsored National Level **Summer School** on **“Microwave Integrated Circuits”** during 16th – 20th June 2008 (**One Week**), at SVNIT, Surat.
2. Attended **13th Annual Convention of ISTE Gujarat Section and One Day Seminar** on **“Autonomy and Accountability of Technical Education in changing scenario”** organized by ISTE on 18th October 2008, at Atmiya institute of Technology and science, Rajkot.
3. Attended **One Day symposium** on **“SWOT Analysis of Training & Placement Cell”** jointly organized by IIPC & ISTE Chapter on 6th March 2009, at B. & B. Institute of Technology, Vallabh Vidyanagar.
4. Attended AICTE sponsored **STTP** on **“Microcontrollers & their Applications”** during 14th – 18th December 2009 organized by Department of Electrical Engineering, at SVNIT, Surat.
5. Attended DTE – ISTE approved **STTP** on **“Circuit Simulation Tools & PCB Design Concepts”** during 4th – 8th January 2010 jointly organized by ISTE Gujarat Section & B. & B. Institute of Technology, Vallabh Vidyanagar.
6. Attended DTE – ISTE approved **STTP** on **“Web Development & Web Hosting”** during 17th – 21st May 2010 jointly organized by Information Engineering Dept. BBIT Vallabh Vidyanagar & ISTE Gujarat Section at B. & B. Institute of Technology, Vallabh Vidyanagar.
7. Attended **One Day Regional Seminar** on **“XXI Century: Priorities in Technical & Higher Education”** organized by C.V.M. in associated with ISTE Gujarat Section & G.T.U. on 3rd August 2011.
8. Attended AICTE sponsored **STTP** on **“Pedagogy”** during 19th – 30th December 2011 organized by B. & B. Institute of Technology, Vallabh Vidyanagar.
9. Attended **One Day Workshop** on **“Antennas, RF circuits and Satellites Communications”** on 25th August 2012 with technical support from AP/MTT chapter of IEEE Gujarat Section at A.D.I.T. New Vallabh Vidyanagar.
10. Attended **One Day Symposium** on **“Communication Technologies - 2012”** on 3rd November 2012 jointly organized by GCET EC Department & GCET IEEE Students Branch at G.C.E.T. Vallabh Vidyanagar.

11. Attended **One day Workshop** on “**Design of Transmission line, High frequency filters and Microstrip patch antenna using ADS software**” on 8th March 2013 jointly organized by GCET EC Department & ISTE at G.C.E.T. Vallabh Vidyanagar.
12. Attended **One Day Seminar** on “**MATLAB & Simulink for Engineering Education**” on 20th December 2013 organized by MATLAB India at Madhubhan Resort, Anand 388001.
13. Attended AICTE sponsored **Two Days National Seminar** on “**Recent Trends in VLSI**” on 18th – 19th January 2014 organized by E. & C. Engineering Department of Parul Institute of Engineering & Technology – DS, Waghodia, Vadodara.
14. Attended TEQIP-II sponsored **Three Days Workshop** program on “**Recent Advancements in Antennas & Metamaterials**” organized by Electronics Engineering Department, SVNIT, Surat during 27th to 29th March 2014.
15. Attended STTP on “**Recent Trends in Communication, Computing and Networking**” during 19th – 23rd May 2014 organized by GCET EC Department at G.C.E.T. Vallabh Vidyanagar.
16. Arranged Two days workshop for Students on “**Arduino and its application**” during 12-13 February 2016 organized by EC Department at B.B.I.T Vallabh Vidyanagar under ISTE student chapter.
17. Arranged Two days workshop for students on “**Microcontroller and its Applications**” in April 2016 organized by EC Department at B.B.I.T. Vallabh Vidyanagar under ISTE student chapter.
18. Arranged & Attended ISTE (New Delhi) approved CVM & IE (I) sponsored **One Week STTP** program on “**IoT with Pi-Duino**” organized by E. & C. Engg. Dept., B.B.I.T. (GIA), V. V. Nagar during 17th to 22nd April 2017.

CO-CURRICULAR ACTIVITIES

- Worked as an Academic review committee member at Parul University (03/03/2018)
- Worked as a paper setter and external examiner for Polytechnic at M.S.University.
- Worked as a paper setter for IETE, New Delhi.
- Worked as a Committee Member for NBA Accreditation.
- Worked as a Committee Member for online Central admission.
- Worked as a Committee Member for Time Table.
- Worked as a Committee Member for Annual function in Institute.
- Worked as a Observer and Squad Member during GTU Examination of Zone – 3.
- Worked as a Paper setter for GTU external examination.
- Worked as an external examiner for practical examination of GTU.
- Worked as Electronics group Co-Ordinator for National level Project competition CREATO in 2015, 2016 and 2017.

PERSONAL SOCIETY MEMBERSHIP

- ✓ ISTE LIFE MEMBERSHIP : LM 70514
- ✓ IAENG MEMBERSHIP : 139561

PERSONAL DATA

DATE OF BIRTH : 30th July 1984
PLACE OF BIRTH : Vadodara, Gujarat, India
LANGUAGES : English, Gujarati, Hindi
MARITAL STATUS : Married

REFEREES

Prof. Mukul Y. Kantharia (e-mail: my_kant@gmail.com; phone: +919428437121)
Professor & Head, Electronics & Communication Engineering,
Bhailalbai & Bhikhabhai Institute of Technology,
Gujarat Technological University, Vallabh Vidyanagar, Gujarat, India
Prof. Kantharia is Head of the Department and Colleague.

Prof. R. D. Patel (e-mail: rakeshgtu@gmail.com; phone: +919825523982)
Professor & Head, Mechanical Engineering,
Bhailalbai & Bhikhabhai Institute of Technology,
Gujarat Technological University, Vallabh Vidyanagar, Gujarat, India
Prof. Patel is Colleague.

Prof. Jigar M. Patel (e-mail: jmpatel@bbit.ac.in; phone: +919904204878)
Lecturer, Electronics & Communication Engineering,
Bhailalbai & Bhikhabhai Institute of Technology,
Gujarat Technological University, Vallabh Vidyanagar, Gujarat, India
Prof. Jigar is Colleague & collaborator on my Antenna design work.
