

CURRICULUM – VITAE

Mr. Muktesh Prakashbhai Shah

Date of Birth: July 30, 1984

Indian, Male, Married

Permanent Address:

F-50, Yogishwar Society, Near Padam Park, New era School road, Makarpura,
Vadodara – 390010, Gujarat, India.

Ph.No.: +91-265-2630656 (H) +91-9428437704 (M)

Email: shahmuktesh@gmail.com

Present Status

- Presently working as a Lecturer in E. & C. Engineering Department, B. & B. Institute of Technology, Vallabh Vidyanagar since 9th July 2007.

Work Experience

- Total teaching experience is 7 years.

Academics

M. E. (Communication Engineering)

G. H. Patel College of Engineering & Technology, Vallabh Vidyanagar (G.T.U.)
Pursuing

Thesis Title

“Design and Development of Multiband Microstrip Radiating Structure using CSRR with Curve tooth”.

B.E. (Electronics & Communication Engineering)

South Gujarat University
passed in June, 2005.
Percentage : 71.61 %

Skill Set:

Simulation Tool

MATLAB, HFSS, ADS, Quartus-II, Multisim, Orcad, NEC

Protocol

TCP/IP, SNMPv1, SNMPv2, SNMPv3.

Assembly Language

8085 and 8086 Microprocessor, 8051 Micro controller.

Publication Details

International Journal Papers:

1. **M. P. Shah**, S. K. Patel, M. A. Ardeshana, J. M. Patel, “Design of Multiband Microstrip Radiating Structure for C band Application”, International Journal of Advanced Research in Computer & Communication Engineering (IJARCCE), Vol – 2, Issue 12, Dec. 2013.

Workshop / Training Programme / Seminar / STTP Attended

1. Attended AICTE sponsored National Level **Summer School** on “**Microwave Integrated Circuits**” during 16th – 20th June 2008 (**One Week**), at SVNIT, Surat.
2. Attended **13th Annual Convention of ISTE Gujarat Section and One Day Seminar** on “**Autonomy and Accountability of Technical Education in changing scenario**” organized by ISTE on 18th October 2008, at Atmiya institute of Technology and science, Rajkot.
3. Attended **One Day symposium** on “**SWOT Analysis of Training & Placement Cell**” jointly organized by IIPC & ISTE Chapter on 6th March 2009, at B. & B. Institute of Technology, Vallabh Vidyanagar.
4. Attended AICTE sponsored **STTP** on “**Microcontrollers & their Applications**” during 14th – 18th December 2009 organized by Department of Electrical Engineering, at SVNIT, Surat.
5. Attended DTE – ISTE approved **STTP** on “**Circuit Simulation Tools & PCB Design Concepts**” during 4th – 8th January 2010 jointly organized by ISTE Gujarat Section & B. & B. Institute of Technology, Vallabh Vidyanagar.
6. Attended DTE – ISTE approved **STTP** on “**Web Development & Web Hosting**” during 17th – 21st May 2010 jointly organized by Information Engineering Dept. BBIT Vallabh Vidyanagar & ISTE Gujarat Section at B. & B. Institute of Technology, Vallabh Vidyanagar.
7. Attended **One Day Regional Seminar** on “**XXI Century: Priorities in Technical & Higher Education**” organized by C.V.M. in associated with ISTE Gujarat Section & G.T.U. on 3rd August 2011.
8. Attended AICTE sponsored **STTP** on “**Pedagogy**” during 19th – 30th December 2011 organized by B. & B. Institute of Technology, Vallabh Vidyanagar.
9. Attended **One Day Workshop** on “**Antennas, RF circuits and Satellites Communications**” on 25th August 2012 with technical support from AP/MTT chapter of IEEE Gujarat Section at A.D.I.T. New Vallabh Vidyanagar.
10. Attended **One Day Symposium** on “**Communication Technologies - 2012**” on 3rd November 2012 jointly organized by GCET EC Department & GCET IEEE Students Branch at G.C.E.T. Vallabh Vidyanagar.
11. Attended **One day Workshop** on “**Design of Transmission line, High frequency filters and Microstrip patch antenna using ADS software**” on 8th March 2013 jointly organized by GCET EC Department & ISTE at G.C.E.T. Vallabh Vidyanagar.
12. Attended **One Day Seminar** on “**MATLAB & Simulink for Engineering Education**” on 20th December 2013 organized by MATLAB India at Madhubhan Resort, Anand 388001.

Subject Taught At Diploma Level

- Electronics Networks & Lines.
- Telecommunication Techniques & Application.
- Mobile Communication.
- Antenna & Wave Propagation.
- Microprocessor & Assembly Language Programming
- Principles of Electronic Communication
- Communication Engineering – II
- Consumer Electronics
- PCB Designing Tools
- Project - 1
- Project – 2

Area of Interest

- RF & Antenna Design.
- Wireless & Mobile Communication.
- Image Processing.

Co-Curricular Activities

- Worked as Member Committee for NBA Accreditation.
- Worked as Member Committee for Online Central Admission.
- Worked as Member Committee for Time Table.
- Worked as Member Committee for Annual Function in Institute.

Personal Society Membership

- ISTE LIFE MEMBERSHIP

Reference

Mr. M. Y. Kantharia
I/C H.O.D.
E. & C. Engineering Department
B. & B. Institute of Technology, V. V. Nagar
Ph.No.: +91-9428437121
Email: my_kant@yahoo.com

Dr. I. N. Patel
Professor
Structural Engineering Department
B. V. M. Engineering College, V. V. Nagar
Ph.No.: +91-9898034464
Email: inpatel34@gmail.com

Updated on: 06/01/2014