

## CURRICULUM – VITAE

**Mr. Jigar Maheshbhai Patel**

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**Date of Birth:** September 17, 1983

Indian, Male, Married

**Permanent Address:**

“Jalaram Smruti”, Nr. Azad Medan, Anand – 388001, Gujarat, India.

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### **Present Status**

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- Presently working as a Lecturer in E. & C. Engineering Department, B. & B. Institute of Technology, Vallabh Vidyanagar since 2<sup>nd</sup> January 2007.

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### **Work Experience**

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- Total teaching experience is 7 years.

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### **Academics**

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**M. E. (Communication Engineering)**

G. H. Patel College of Engineering & Technology, Vallabh Vidyanagar (G.T.U.) passed in July 2013.  
**CPI: 8.36**

**Thesis Title**

“Design of Multiband Microstrip Patch Antenna”.

**B.E. (Electronics & Communication Engineering)**

Charotar Institute of Technology, Changa (Gujarat University) passed in June, 2005.  
**Percentage (Overall): 65.58 %**

**Project Work**

In final year of B.E., I have designed Yagi Uda Antenna. This project is a hardware design of the Yagi Uda Antenna. I have done this project for Oritron Electronics Corporation, Ahmedabad.

**H.S.C. (10 + 2)  
(Science Stream)**

Gujarat Higher Secondary Education Board  
**Percentage: 80.46%**

March-April 2001.

**S.S.C. (10<sup>th</sup>)**

Gujarat Secondary Education Board

**Percentage:** 82.57%

March 1999.

**Skill Set:**

**Languages**

C, C++.

**Simulation Tool**

MATLAB, HFSS, ADS, Quartus-II, Multisim, Orcad, NEC

**Protocol**

TCP/IP, SNMPv1, SNMPv2, SNMPv3.

**OS**

Windows, DOS.

**Assembly Language**

8085 and 8086 Microprocessor, 80C51 Micro controller.

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**Publication Details**

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**International Journal Papers:**

1. J. M. Patel, S. K. Patel, F. N. Thakkar, "Defected Ground Structure Multiband Microstrip Patch Antenna Using Complementary Split Ring Resonator", International Journal of Emerging Trends in Electrical and Electronics (IJETEE), Vol- 3, Issue-2, May 2013.
2. J. M. Patel, S. K. Patel, F. N. Thakkar, "Comparative Analysis of S-Shaped Multiband Microstrip Patch Antenna", International Journal of Advanced Research Electrical, Electronics and Instrumentation Engineering (IJAREEIE), Vol- 2, Issue-7, July 2013.
3. J. M. Patel, S. K. Patel, F. N. Thakkar, "Design of Defected Ground Structure S-Shaped Multiband Microstrip Patch Antenna using Complementary Split Ring Resonator ", International Journal of Advanced and Innovative Research (IJAIR), Vol- 3, Issue- 1, July 2013.
4. M. P. Shah, S. K. Patel, M. A. Ardeshana, J. M. Patel, "Design of Multiband Microstrip Radiating Structure for C band Application", International Journal of Advanced Research in Computer & Communication Engineering (IJARCCE), Vol – 2, Issue 12, Dec. 2013

**International Conference Papers:**

1. J. M. Patel, S. K. Patel, F. N. Thakkar, "Design of S-Shaped Multiband Microstrip Patch Antenna", International Conference on Engineering (NUiCONE-2012), 6-8 Dec. 2012.

**National Conference Papers:**

1. J. M. Patel, S. K. Patel, F. N. Thakkar, "Simulation of Microstrip Transmission Line Using ADS," National Conference on Innovative & Emerging Technologies (NCIET 2013), (ISBN 978-81-925650-0-2), pp. 444-448, 24-25 Jan. 2013.

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**Workshop / Training Programme / Seminar / STTP Attended**

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1. Attended 12<sup>th</sup> Annual Convention of ISTE, Gujarat Section & **One Day Symposium on “Excellence in Technical Education Through Innovation”** on 20<sup>th</sup> October 2007, at B.V.M. (Engineering College), Vallabh Vidyanagar.
2. Attended AICTE sponsored National Lever **Summer School on “Microwave Integrated Circuits”** during 16<sup>th</sup> – 20<sup>th</sup> June 2008 (**One Week**), at SVNIT, Surat.
3. Attended **One Day symposium on “SWOT Analysis of Training & Placement Cell”** jointly organized by IIPC & ISTE Chapter on 6<sup>th</sup> March 2009, at B. & B. Institute of Technology, Vallabh Vidyanagar.
4. Attended **one day workshop** program on **“MATLAB & Application for Engineering Subjects”** organized by Department of Electronics & Communication Engineering on 30<sup>th</sup> March 2009, at Faculty of Engineering, D.D.U., Nadiad.
5. Attended AICTE sponsored **STTP on “Microcontrollers & their Applications”** during 14<sup>th</sup> – 18<sup>th</sup> December 2009 organized by Department of Electrical Engineering, at SVNIT, Surat.
6. Attended DTE – ISTE approved **STTP on “Circuit Simulation Tools & PCS Design Concepts”** during 4<sup>th</sup> – 8<sup>th</sup> January 2010 jointly organized by ISTE Gujarat Section & B. & B. Institute of Technology, Vallabh Vidyanagar.
7. Attended DTE – ISTE approved **STTP on “Web Development & Web Hosting”** during 17<sup>th</sup> – 21<sup>st</sup> May 2010 jointly organized by Information Engineering Dept. BBIT Vallabh Vidyanagar & ISTE Gujarat Section at B. & B. Institute of Technology, Vallabh Vidyanagar.
8. Attended **One Day Regional Seminar on “XXI Century: Priorities in Technical & Higher Education”** organized by C.V.M. in associated with ISTE Gujarat Section & G.T.U. on 3<sup>rd</sup> August 2011.
9. Attended **One Day Workshop on “Digital Image Processing & MATLAB Programming”** on 27<sup>th</sup> March 2012 organized by Computer Engineering Department in association with ISTE chapter, A.D.I.T. New Vallabh Vidyanagar.
10. Attended **One Day Workshop on “Antennas, RF circuits and Satellites Communications”** on 25<sup>th</sup> August 2012 with technical support from AP/MTT chapter of IEEE Gujarat Section at A.D.I.T. New Vallabh Vidyanagar.
11. Attended **One Day Symposium on “Communication Technologies - 2012”** on 3<sup>rd</sup> November 2012 jointly organized by GCET EC Department & GCET IEEE Students Branch at G.C.E.T. Vallabh Vidyanagar.

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**National / Inter National Conferences Attended**

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- Participated, Published & Presented a paper titled **“Design of S-Shaped Multiband Microstrip Patch Antenna”** at 3<sup>rd</sup> Nirma University **International Conference on Engineering (NUICONE-2012)**, 6-8 Dec. 2012 at Nirma University Ahmedabad.
- Participated, Published & Presented a paper titled **“Simulation of Microstrip Transmission Line Using ADS”** at **National Conference on Innovative & Emerging Technologies (NCIET 2013)**, 24-25 Jan. 2013 at Smt. S. R. Patel Engineering College, Dabhi, Mehsana, Gujarat.

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### **Subject Taught At Diploma Level**

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- Electronics Devices & Circuits – I.
- Programming in ‘C’.
- Electronics Devices & Circuits – II.
- Digital Electronics.
- Electronics Networks & Lines.
- Television Engineering.
- Fiber Optic Communication.
- Microwave Engineering.
- Telecommunication Techniques & Application.
- Mobile Communication.
- Antenna & Wave Propagation.
- Electronic Components & Practice.

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### **Area of Interest**

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- RF & Antenna Design.
- Wireless & Mobile Communication.
- Image Processing.

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### **Co-Curricular Activities**

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- Worked as Member Committee for NBA Accreditation.
- Worked as Member-Coordinator Committee for Dr C L Patel Platinum Jubilee Celebration.
- Worked as Member Committee for Online Central Admission for Three Years (2008 - 2011).
- Worked as NODAL Officer for NRI/MQ Admission (2011).
- Worked as Member Committee for Time Table.
- Worked as Member Committee for Annual Function in Institute (2011-2012).

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### **Personal Society Membership**

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- ISTE LIFE MEMBERSHIP: LM 55514

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### **Reference**

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Mr. M. Y. Kantharia  
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